	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	7676	(cpu or processor or microcontroller or microprocessor or controller)with(handler)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:17
2	BRS	L2	108	1 with (policy or policies or rule or right or privilege or permission or acl)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:46
3	BRS	L3	2076	1 with (rout$3 or forward$3 or pass$3 or relay$3 or manag$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:17
4	BRS	L5	221	1 with(without or lack$3 or absent or withdraw$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:18
5	BRS	L6	4	5 with(security or resource or policy or policies or rule or right or privilege or permission or acl)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:19
6	BRS	L4	41	2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:44

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L8	23872	(719/318,328 or 726/1,2,3,4,11,12,13, 14,21 or 713/150,168,169,175, 176 or 709/204,223,224,225, 226,227,228,229).ccl s.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:45
8	BRS	L9	268	1 same (policy or policies or rule or right or privilege or permission or acl)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:46
9	BRS	L10	21	8 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/14 09:46